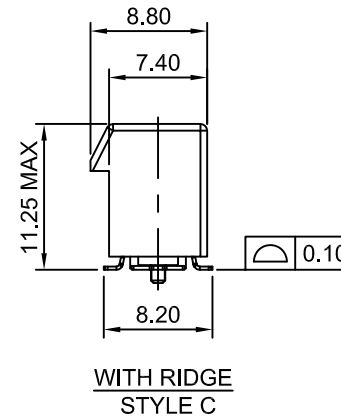
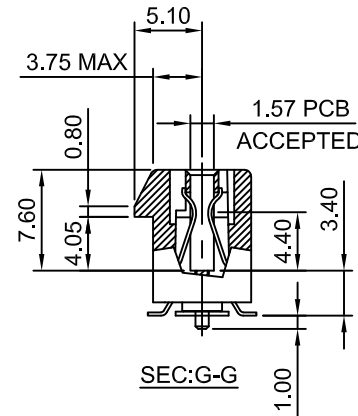
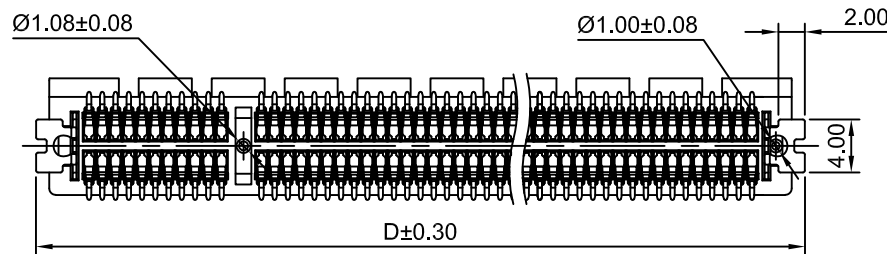
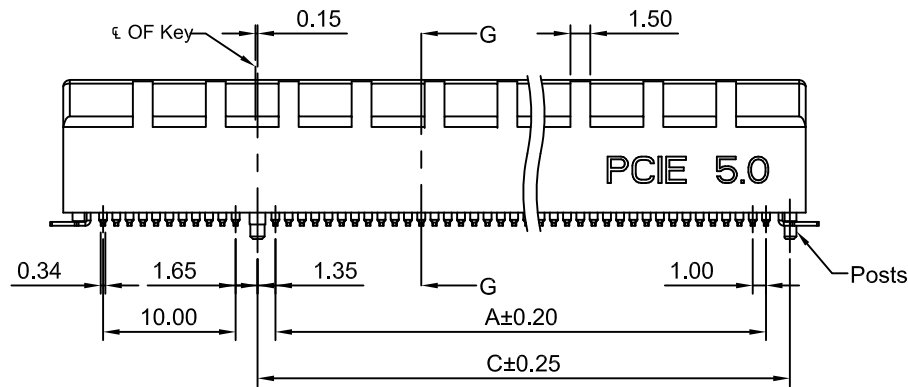
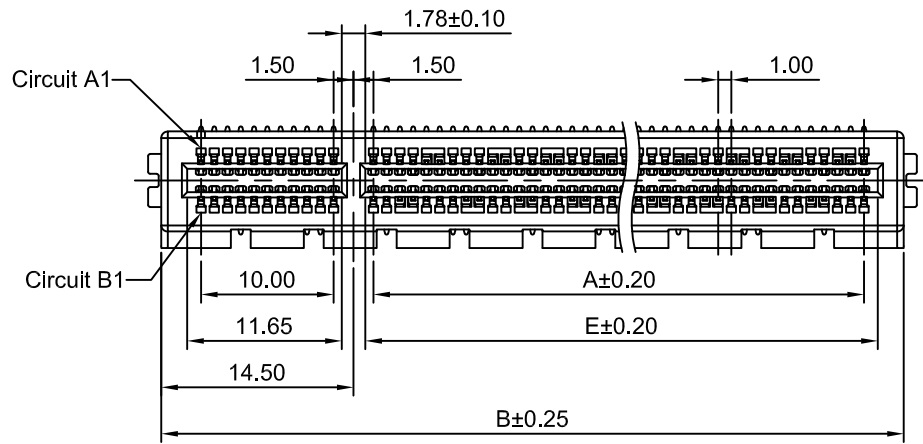


Ordering Information:

PCIE5 - 1001WVX -S- X -XP- ** - **
1 2 3 4 5

- Part NO.
- Ridge Options Style
B: Style B With Ridge
C: Style C Non Ridge
- Locating Posts Style:
(Blank): None Locating Posts
E: With Posts
- Finish:
(Blank): Select Gold:15µin[0.38µm]Min.
Select reflowed matte tin: 80µin[2.03µm] Min
Overall Nickel Underplate: 30µin[0.76µm] Min.
G: Select Gold:1uin[0.025µm]Min.
Select reflowed matte tin: 80µin[2.03µm] Min
Overall Nickel Underplate: 30µin[0.76µm] Min.
G30: Select Gold:30µin[0.76µm]Min.
Select reflowed matte tin: 80µin[2.03µm] Min
Overall Nickel Underplate: 30µin[0.76µm] Min.
- Packing:
(Blank): Tray
CTR: Tray Packaging With Cap
MTR: Tray Packaging With Mylar
RE: Tape & Reel Packaging
CRE: Tape & Reel Packaging With Cap
MRE: Tape & Reel Packaging With Mylar
HTR: Hard Tray
MHTR: Hard Tray (338x235) With Mylar
CHTR: Hard Tray (338x235) With Cap

SPECIFICATIONS:

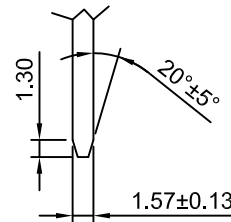
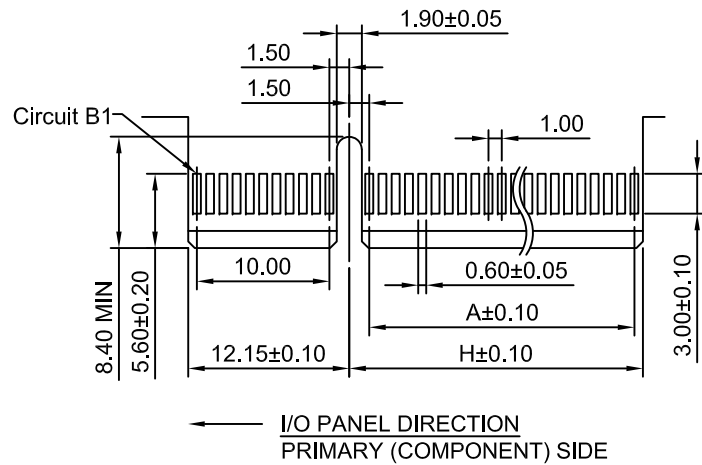
Current rating: 1.1A AC,DC
Voltage rating: 50V AC,DC
Temperature range: -40°C ~ +105°C
Contact resistance: 40mΩ Max.
Insulation resistance: 100MΩ Min.
Withstanding voltage: 500V AC/minute
Pin Material: Phosphor Bronze
Solder tabs: Brass/Matte Tin over Nickel
CAP Material: LCP,UL94V-0(Black)
Housing Material: Nylon 9T,UL94V-0 Halogen Free(Black)

Ordering Information & Dimensions

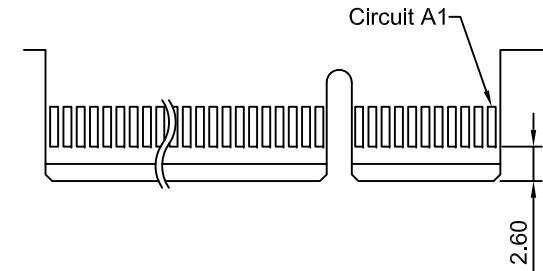
Circuits	Dimensions(mm)				
	A	B	C	D	E
36P	6.00	25.00	9.15	27.00	7.65
64P	20.00	39.00	23.15	41.00	21.65
98P	37.00	56.00	40.15	58.00	38.65
164P	70.00	89.00	73.15	91.00	71.65

CJTconn CHANGJIANG CONNECTORS CO.,LTD.

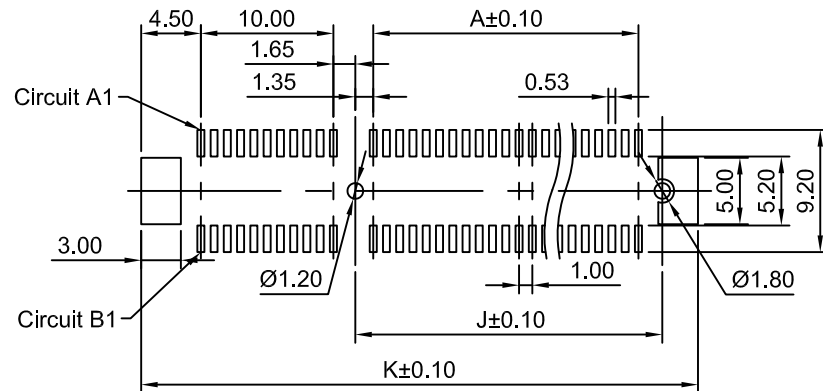
Unit	Scale	Sheet	TITLE	
mm	2:1	1 of 3	1.00mm Pitch	
DRAWN	CHK'D	APPR'D	PCI-E CARD EDGE GEN5 SMT	
Haisen Li	Diankui Wan	Jack Yin	DRAWING NO.	
General Tolerances			THIRD ANGLE PROJECTION	
x.xx±0.25 x.xxx±0.20 x.xxxx±0.15 ANGLES ±1°			PCIE5-1001WVX-S-X-XP	
A0	NEW RELEASE	2025.05.05	REV A0	
REV	DESCRIPTION	DATE		



NOTES:
CHAMFER EDGE MUST BE
FREE OF CUTTING BURRS



SECONDARY (SOLDER) SIDE



RECOMMENDED PCB LAYOUT
(COMPONENT SIDE)
TOLERANCE: ±0.05

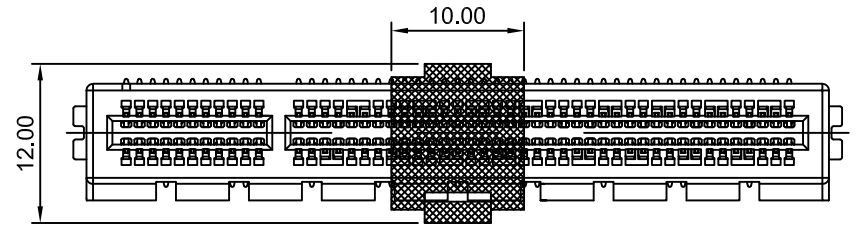
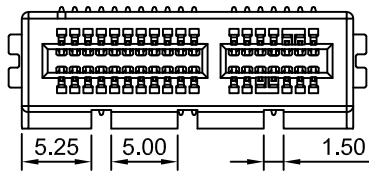
Ordering Information & Dimensions

Circuits	Dimensions(mm)			
	A	H	J	K
36P	6.00	8.15	9.15	28.00
64P	20.00	22.15	23.15	42.00
98P	37.00	39.15	40.15	59.00
164P	70.00	72.15	73.15	92.00

CJTconn CHANGJIANG CONNECTORS CO.,LTD.

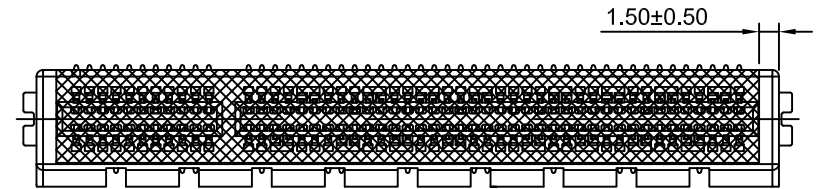
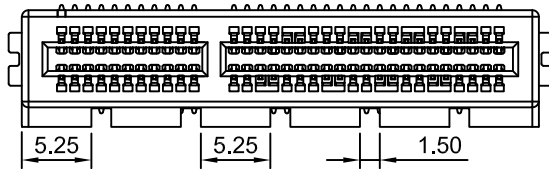
Unit	Scale	Sheet	TITLE	
mm	2:1	2 of 3	1.00mm Pitch PCI-E CARD EDGE GEN5 SMT	
DRAWN	CHK'D	APPR'D		
Haisen Li	Diankui Wan	Jack Yin	DRAWING NO.	
General Tolerances			THIRD ANGLE PROJECTION	
x.xx±0.25 x.xxx±0.20 x.xxxx±0.15 ANGLES ±1°			PCIE5-1001WVX-S-X-XP	
A0	NEW RELEASE	2025.05.05	REV A0	
REV	DESCRIPTION	DATE		

36P



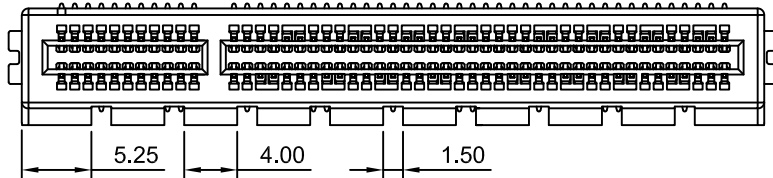
WITH CAP

64P

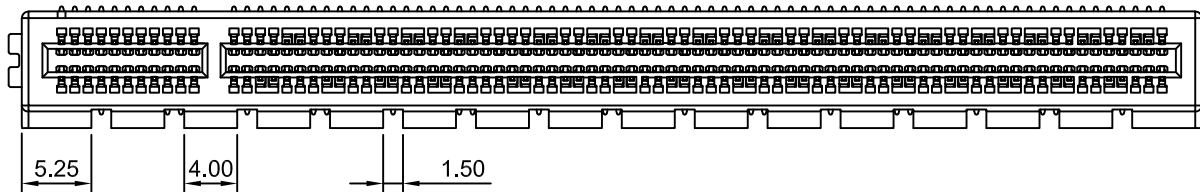


WITH MYLAR

98P



164P



CJTconn CHANGJIANG CONNECTORS CO.,LTD.			
Unit	Scale	Sheet	TITLE
mm	2:1	3 of 3	
DRAWN	CHK'D	APPR'D	1.00mm Pitch PCI-E CARD EDGE GEN5 SMT
Haisen Li	Diankui Wan	Jack Yin	
General Tolerances		THIRD ANGLE PROJECTION	DRAWING NO.
x.xx±0.25 x.xxx±0.20 x.xxxx±0.15 ANGLES ±1°			PCIE5-1001WVX-S-X-XP
A0	NEW RELEASE	2025.05.05	REV A0
REV	DESCRIPTION	DATE	