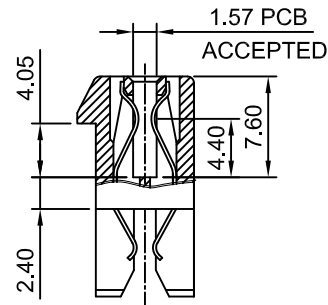
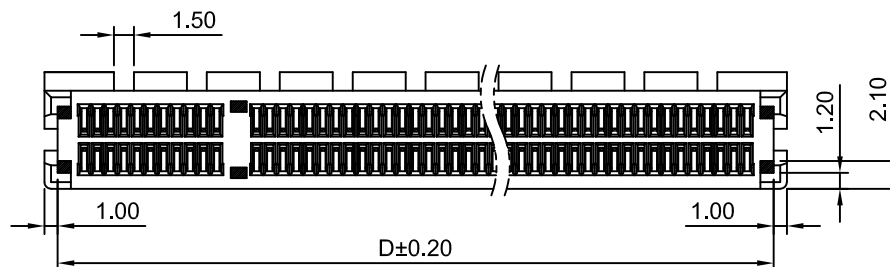
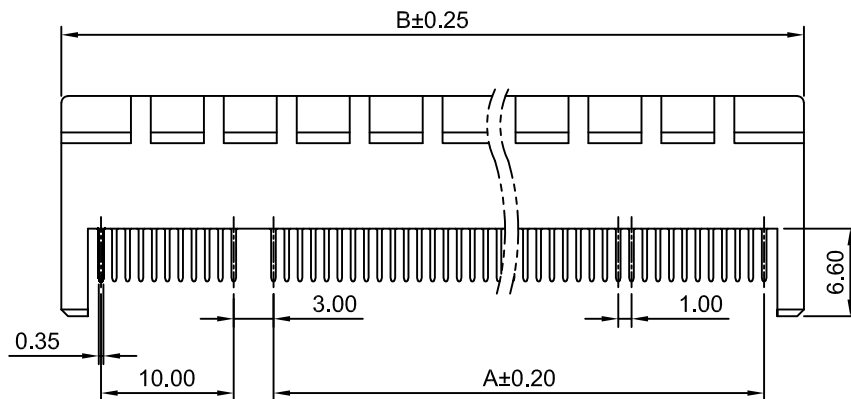
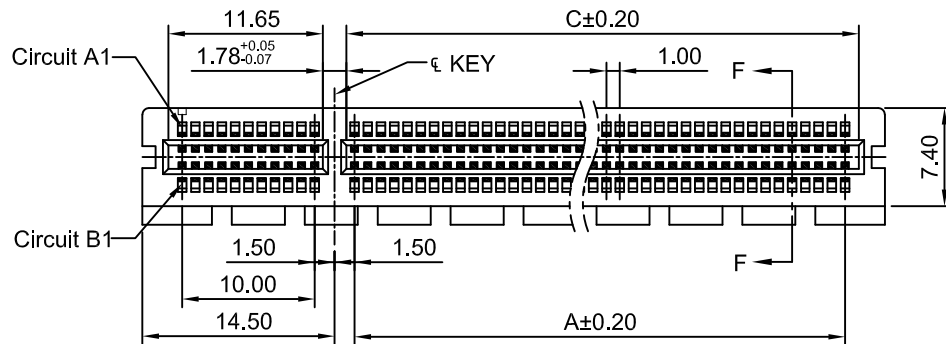
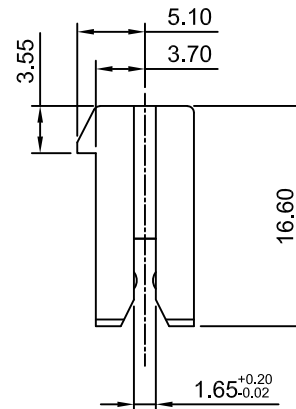




SECTION:F-F



Ordering Information:

PCIE-1001WVC-NF-XP - ** - ** - **
1 2 3 4

1.Part NO.

2.Color:

(Blank): Natural

Bk: Black

3.Finish:

(Blank): Select Gold:15µin[0.38µm]Min.

Select reflowed matte tin: 80µin[2.03µm] Min

Overall Nickel Underplate: 30µin[0.76µm] Min.

G: Select Gold:1uin[0.025um]Min.

Select reflowed matte tin: 80µin[2.03µm] Min

Overall Nickel Underplate: 30µin[0.76µm] Min.

G30: Select Gold:30µin[0.76µm]Min.

Select reflowed matte tin: 80µin[2.03µm] Min

Overall Nickel Underplate: 30µin[0.76µm] Min.

4.Packing:

(Blank): Tray With Cap

SPECIFICATIONS:

Current rating: 1.1A AC,DC

Voltage rating: 50V AC,DC

Temperature range: -40°C ~ +105°C

Contact resistance: 40mΩ Max.

Insulation resistance: 100MΩ Min.

Withstanding voltage: 500V AC/minute

Pin Material: Phosphor Bronze

Dummy Card Material: LCP

Housing Material: Nylon 9T,UL94V-0

Ordering Information & Dimensions

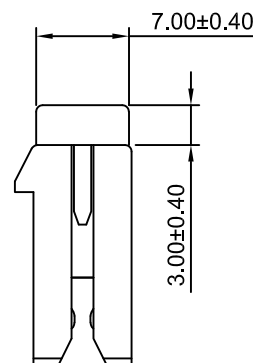
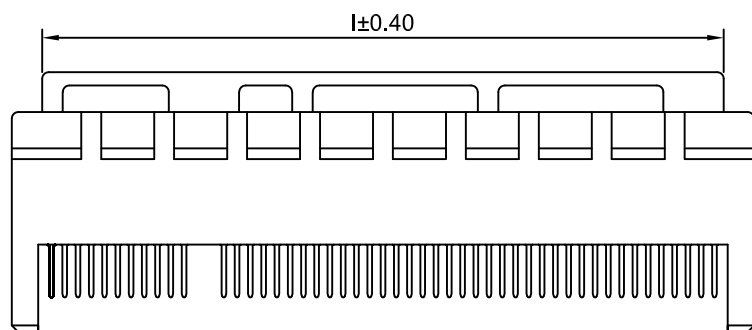
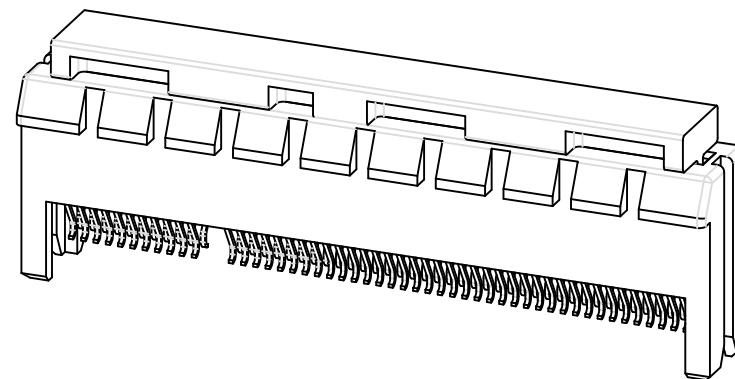
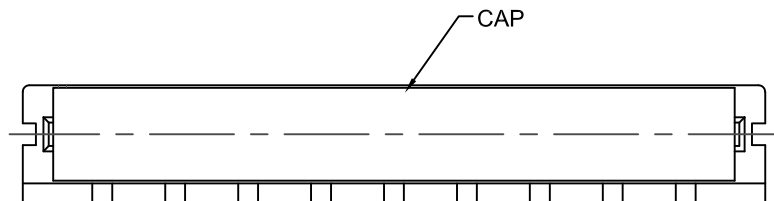
Circuits	Dimensions(mm)				
	A	B	C	D	I
36P	6.00	25.00	7.65	23.00	20.40
64P	20.00	39.00	21.65	37.00	34.40
98P	37.00	56.00	38.65	54.00	51.40
164P	70.00	89.00	71.65	87.00	84.40

CJTconn

CHANGJIANG CONNECTORS CO.,LTD.

Unit	Scale	Sheet	TITLE	
mm	2:1	1 of 3	PCI EXPRESS CARD EDGE CONNECTOR STRADDLE MOUNT TYPE	
DRAWN	CHK'D	APPR'D	DRAWING NO.	
Haisen Li	Diankui Wan	Jack Yin	PCIE-1001WVC-NF-XP	
General Tolerances			THIRD ANGLE PROJECTION	
x.xx±0.25 x.xxx±0.20 x.xxxx±0.15 ANGLES ±1°			REV A0	

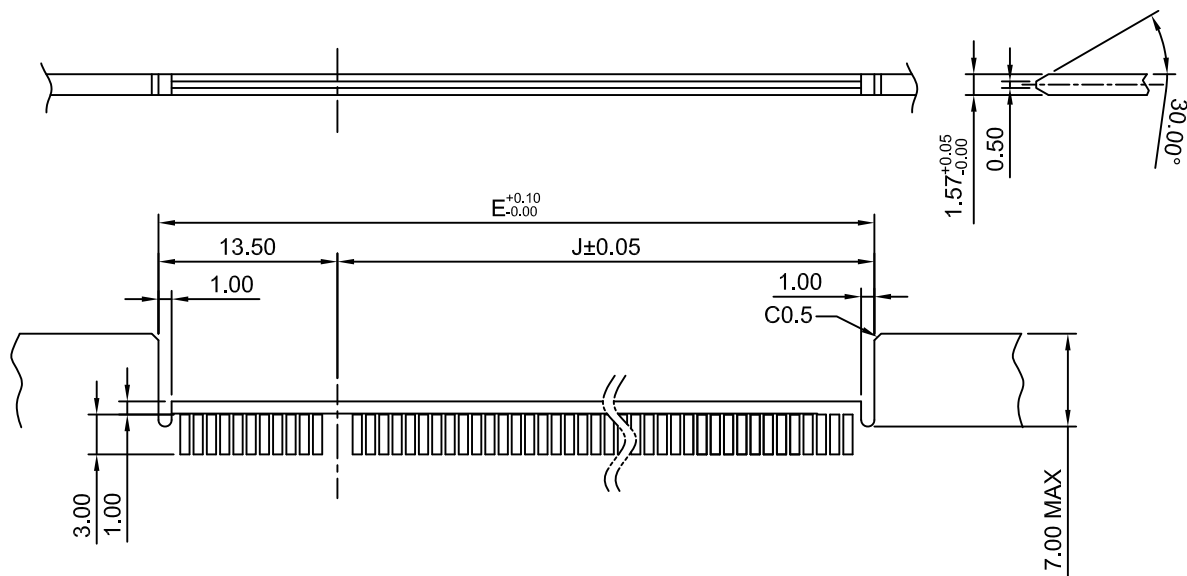
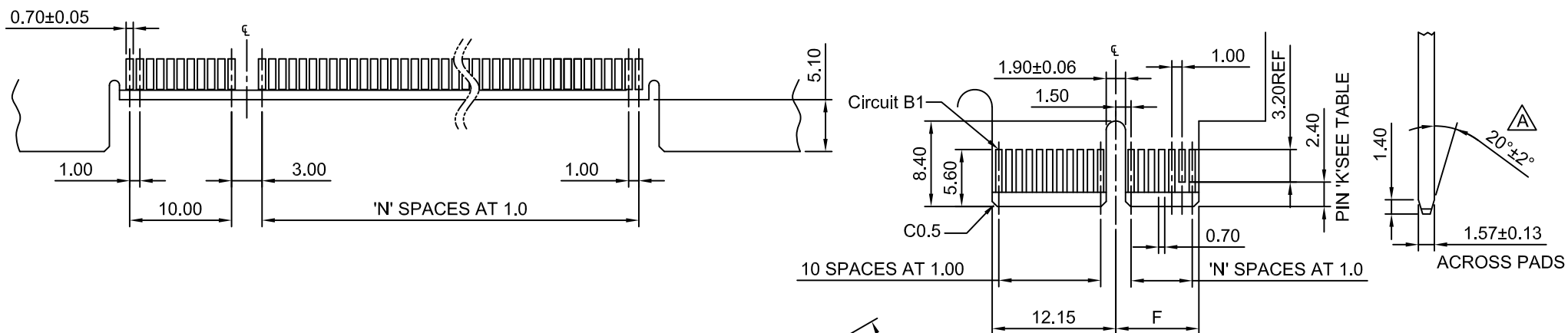
A0	NEW RELEASE	2025.05.05
REV	DESCRIPTION	DATE



ASSEMBLY WITH CAP CONDITION

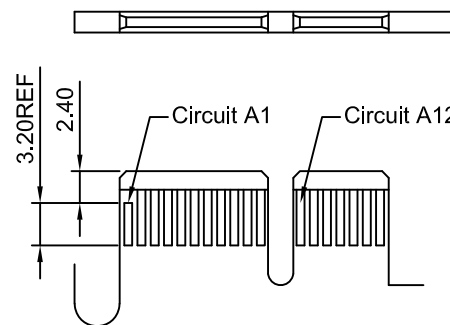
CJTconn CHANGJIANG CONNECTORS CO.,LTD.			
Unit	Scale	Sheet	TITLE
mm	2:1	2 of 3	PCI EXPRESS CARD EDGE CONNECTOR STRADDLE MOUNT TYPE
DRAWN	CHK'D	APPR'D	
Haisen Li	Diankui Wan	Jack Yin	
General Tolerances		THIRD ANGLE PROJECTION	DRAWING NO.
x.x±0.25 x.xx±0.20 x.xxx±0.15 ANGLES ±1°			PCIE-1001WVC-NF-XP
REV	DESCRIPTION		REV A0

A0	NEW RELEASE	2025.05.05
REV	DESCRIPTION	DATE



RECOMMENDED PCB LAYOUT
PCB TOLERANCE: ±0.05

I/O PANEL DIRECTION
PRIMARY (COMPONENT) SIDE



SECONDARY (SOLDER) SIDE
PCI-E ADD-IN CARD EDGE-FINGER DIMENSION.

Ordering Information & Dimensions

Circuits	Dimensions(mm)				
	N	E	J	F	K
36P	6.00	23.00	9.50	8.15	B17
64P	20.00	37.00	23.50	22.15	B17,B31
98P	37.00	54.00	40.50	39.15	B17,B31,B48
164P	70.00	87.00	73.50	72.15	B17,B31,B48,B81

- NOTE:
1. NO TIE BAR PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS A1 AND "K"
 2. CHAMFER EDGE MUST BE FREE OF CUTTING BURRS Δ

A0	NEW RELEASE	2025.05.05
REV	DESCRIPTION	DATE